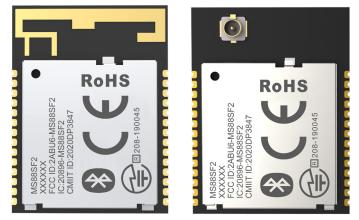


Bluetooth LE Module MS88SF2



Datasheet V 1.0.0

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Version Note

Version	Details	Contributor(s)	Date	Notes
1.0.0	First edit	Michelle, Leo	2024.05.09	

Part Numbers

Model	Hardware Code
MS88SF21	1Y40AI
MS88SF23	3Y40AI

Click the icon to view and download the latest product documents electronically. https://en.minewsemi.com/file/MS88SF2-nRF52840_Datasheet_K_EN.pdf



MS88SF2-nRF52840

High-performance, fully certified, multi-protocol Bluetooth 5.4 module

The MS88SF2 is a low-power, cost-effective wireless BLE 5.4 module based on the nRF52840 SoC. Its powerful 32-bit ARM CortexTM M4F CPU, 1M FLASH program space, 256kB RAM, integrated 2.4 GH transceiver, and other powerful supporting resources provide the perfect solution for Bluetooth connectivity. nRF52840 can support protocols such as ANT, BLE, BLE MESH, ZIGBEE, and THREAD. With a built-in PA/LNA, it has an estimated range of up to 250 meters in open space at a data rate of 125kbps.

FEATURES



Bluetooth 5.4

Fully certified



High-performance





Support ANT, BLE, BLE MESH, ZIGBEE and THREAD protocols, etc

KEY PARAMETER

MS88SF2-nRF52840			
Chip Model	nRF52840	Antenna	PCB/IPEX
Module Size	23.2×17.4×2mm	GPIO	20
Flash	1M	RAM	256KB
Receiving Sensitivity	-95dBm(1Mbps) -103dBm(125kbps)	Transmission Power	-40 ~ +8dBm
Current(TX)	0dBm-4.8mA	Current(RX)	4.6mA

APPLICATION



Smart Buildings



Consumer Electronics



Intelligent Medical care Smart Agriculture



Security

Equipment

Automotive Equipment

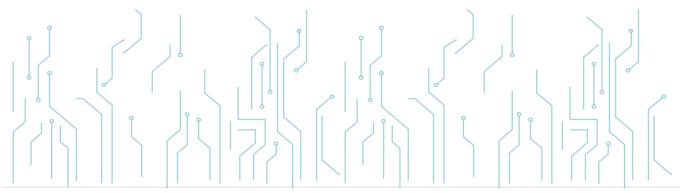
CERTIFICATION







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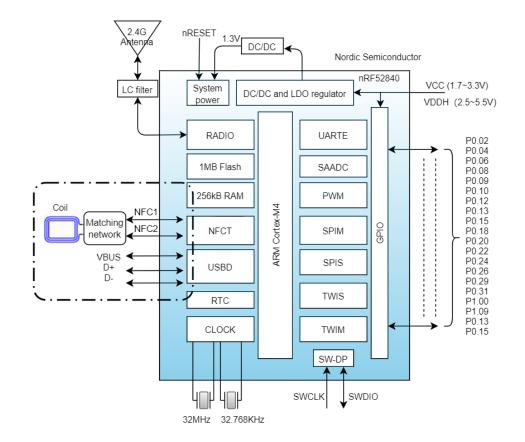


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1 BLOCK DIAGRAM



2 ELECTRICAL SPECIFICATION

Parameter	Values	Notes
Working Voltage	1.7V-5.5V	To ensure RF work, supply voltage suggest not lower than 2.3V
Working Temperature	-40°C ~+85°C	Storage temperature is -40°C ~+125°C
Transmission Power	-40 ~ +8dBm	Configurable
Current(RX)	4.6mA	RF receiving current under 1Mbps pattern
Current(TX)	4.8mA	RF transmission current under odB pattern
Module Dimension	23.2*17.4*2mm	
Quantity of IO Port	20	

05

GND 1 P1.13 2 P0.02 4 P0.29 5 P0.31 6 P0.26 7 P0.04 8 P0.06 9 P0.08 10 P1.09 11 P0.12 12 GND 13 UDD 14

$\mathbf{3}$ pin description

1

PCB Antenna

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• ¢ 16 D-

26 SWCLK

25 SWDIO

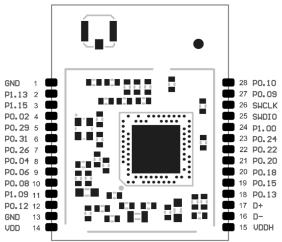
23 SMD10 24 P1.00 23 P0.24 22 P0.22 21 P0.20 20 P0.18 19 P0.15 18 P0.15

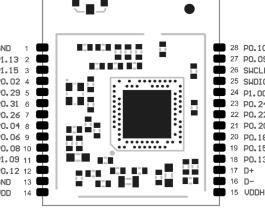
15 VDDH

Ŧ 17 D+

A PIN DEFINITION

Pin Number	Symbol	Туре	Definition
14	VDD	power supply	Power supply, 1.7V-3.6V, use this pin for power supply, need to short VDD and VDDH
15	VDDH	power supply	Power supply, 2.5V-5.5V, use this pin for power supply, not connected to VDD pin
1/13	GND	ground	ground
26/25	SWCLK/SWDIO	Burn Pins	Burn pins, burn only need to connect the power supply pin, ground, and these two pins
2-12/18-24/27-28	P0.02-P0.31 P1.00-P1.09	I/O	General Purpose I/O Port
17	D+	Digital interfaces	USB D+
16	D-	Digital interfaces	USB D-

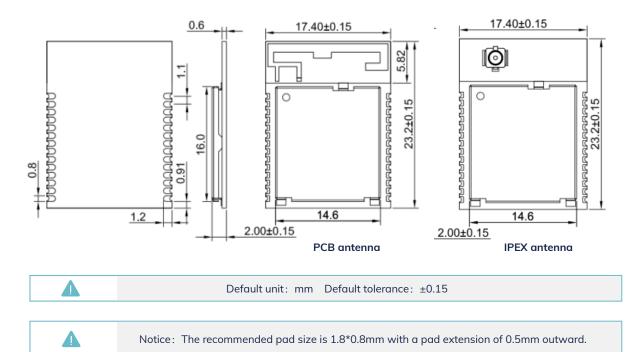




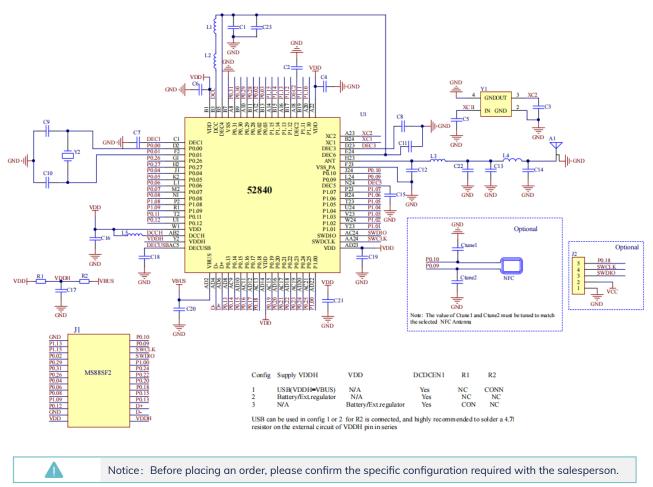
IPEX Antenna

28 PO.10 27 PO.09









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There should be no GND plane or metal cross wiring in the module antenna area, and components should not be placed nearby. It is best to make a hollow or clear area, or place it on the edge of the PCB board.

Notice: Refer to examples as below, and highly suggest to use the first design and the adjustment of modules antenna design according to the first wiring.

Layout Notes:

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1) Preferred Module antenna area completely clearance and not be prevented by metals, otherwise it will influence antenna's effect (as above DWG. indication).

2) Cover the external part of module antenna area with copper as far as possible to reduce the main board's signal cable and other disturbing.

3) It is preferred to have a clearance area of 4 square meter or more area around the module antenna (including the shell) to reduce the influence to antenna.

4) Device should be grounded well to reduce the parasitic inductance.

5) Do not cover copper under module's antenna in order to avoid affect signal radiation or lead to transmission distance affected.

6) Antenna should keep far from other circuits to prevent radiation efficiency reduction or affects the normal operation of other lines.7) Module should be placed on edge of circuit board and keep a distance away from other circuits.

8) Suggesting to use magnetic beads to insulate module's access power supply.

8 REFLOW AND SOLDERING

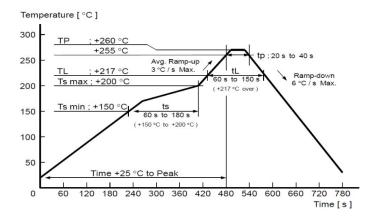
1) Do SMT according to above reflow oven temperature deal curve. Max. Temperature is 260 C;

2) Refer to IPC/JEDEC standard; Peak TEMP<260℃; Times: ≤2 times, suggest only do once reflow soldering on module surface in case of SMT double pad involved. Contact us if special crafts involved.

3) Suggesting to make 0.2mm thickness of module SMT for partial ladder steel mesh, then make the opening extend 0.8mm

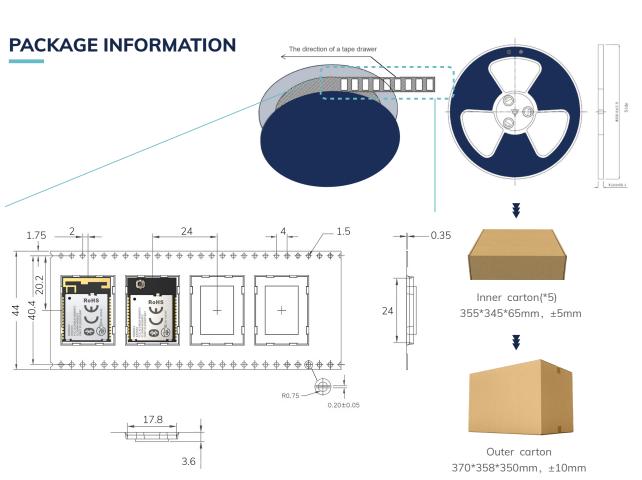
4) After unsealing, it cannot be used up at one time, should be vacuumed for storage, couldn't be exposed in the air for long time. Please avoid getting damp and soldering-pan oxidizing. If there are 7 to 30 days interval before using online SMT, suggest to bake at 65-70 $^{\circ}$ C for 24 hours without disassembling the tape.

5) Before using SMT, please adopt ESD protection measure.





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Remarks

General material list for FCL packaging:



Carrier tape packaging tray



Inner carton(*5) 355*345*65mm, ±5mm

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Humidity Indicator (1 pcs/bag)



Outer carton 370*358*350mm, ±10mm



Desiccant (placed in a vacuum bag)

Other:

Moisture-proof label (attached to the vacuum bag) Certification label (attached to the vacuum bag) Outer box label

Vacuum bag

MS88SF2

Datasheet

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Default unit: mm Default tolerance: ±0.1



LO STORAGE CONDITIONS

- Please use this product within 6 months after signing the receipt.
 - This product should be stored without opening the package at an ambient temperature of $5\sim35^{\circ}$ C and a humidity of $20\sim70\%$ RH.
 - This product should be left for more than 6 months after receipt and should be confirmed before use.
 - The product must be stored in a non-corrosive gas (CI2, NH3, SO2, NOx, etc.).
 - To avoid damaging the packaging material, do not apply any excessive mechanical shocks, including but not limited to sharp objects adhering to the packaging material and product dropping.
- This product is suitable for MSL2 (based on JEDEC standard J-STD-020).
 - After opening the package, the product must be stored at \leq 30°C/<60%RH. It is recommended to use the product within 3-6 months after opening the package.
 - When the color of the indicator in the package changes, the product should be baked before welding.
- Baking is not required for one year if exposure is limited to <30°C and 60%RH. Refer to MSL2 for exposure criteria for moisture sensitivity level. If exposed to (≥168h@85°C/60%RH) conditions or stored for more than one year, recommended baking conditions.

1. 120 +5/-5°C, 8 hours, 1 time

Products must be baked individually on heat-resistant trays because the materials (base tape, reel tape, and cover tape) are not heat-resistant, and the packaging material may be deformed at temperatures of 120° C;

 $2 \times 90^{\circ}C + 8/-0^{\circ}C$, 24hours, 1times

The base tape can be baked together with the product at this temperature. Please pay attention to the uniformity of heat.

11 HANDLING CONDITIONS

• Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

• Handle with care if products may have cracks or damages on their terminals. If there is any such damage, the characteristics of products may change. Do not touch products with bare hands that may result in poor solder ability and destroy by static electrical charge.

Cognizant of our commitment to quality, we operate our own factory equipped with state-of-the-art production facilities and a meticulous quality management system. We hold certifications for ISO9001, ISO14001, ISO27001, OHSA18001, BSCI.

Every product undergoes stringent testing, including transmit power, sensitivity, power consumption, stability, and aging tests. Our fully automated module production line is now in full operation, boasting a production capacity in the millions, capable of meeting high-volume production demands.

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14 RELATED DOCUMENTS

- nRF52840_Chip_Datasheet https://en.minewsemi.com/file/nRF52840_Chip_Datasheet_EN.pdf
- MinewSemi_Product_Naming_Reference_Manual_V1.0 https://en.minewsemi.com/file/MinewSemi_Product_Naming_Reference_Manual_EN.pdf
- MinewSemi_Connectivity_Module_Catalogue_V2.0 https://en.minewsemi.com/file/MinewSemi_Connectivity_Module_Catalogue_EN.pdf



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