



epdiy v7

8 & 16 bit parallel epaper controller
ESP32S3 16 bit octal PSRAM

Designed by Valentin Roland **epdiy** is a powerful driver board for affordable e-Paper (or E-ink) displays. Latest version 7 PCB uses ESP32-S3 microcontroller and LCD module to drive the displays. **In a nutshell this are the features offered:**

- ED133UT2 13.3", 8 bit, 1600 x 1200 displays
- All 9.7", 8 bit, 1200 x 825 displays
- All 40 pin modern E-ink.com / GoodDisplay DES/ Waveshare, 16 bit, different resolutions
- RTC on board: PCF8563
- Professional industry standard PMIC to generate high voltages that this displays need: TPS65185
- Fast ESP32-S3 with 16 MB PSRAM that is enough for most applications. Additionally offering WiFi and BLE using the built in PCB Antenna

Firmware: <https://github.com/vroland/epdiy> Current S3 branch: [s3_lcd](#)

Working Demos that come with the firmware: Dragon (simple C array drawing)
www-images (download JPG and render), Grayscale, Terminal and much more.

FASANI CORP. is fabricating, as an **epdiy collaborator**, a modified version that is also [a fully open source PCB](#) as per license terms only oriented to big displays (≥ 9.7 inches)

Preview and more information at the [tindie](#) sales page

NEW 1.1 Revision has the 13 inch FPC connector J8 rotated 180° to allow the ED133UT2 be connected leaving the USB-C at the edge of the case.
TODO: Design a 13.3" case to place epaper, battery, and optional I2C sensor in a flat 3D-printed case with this PCB as controller.

