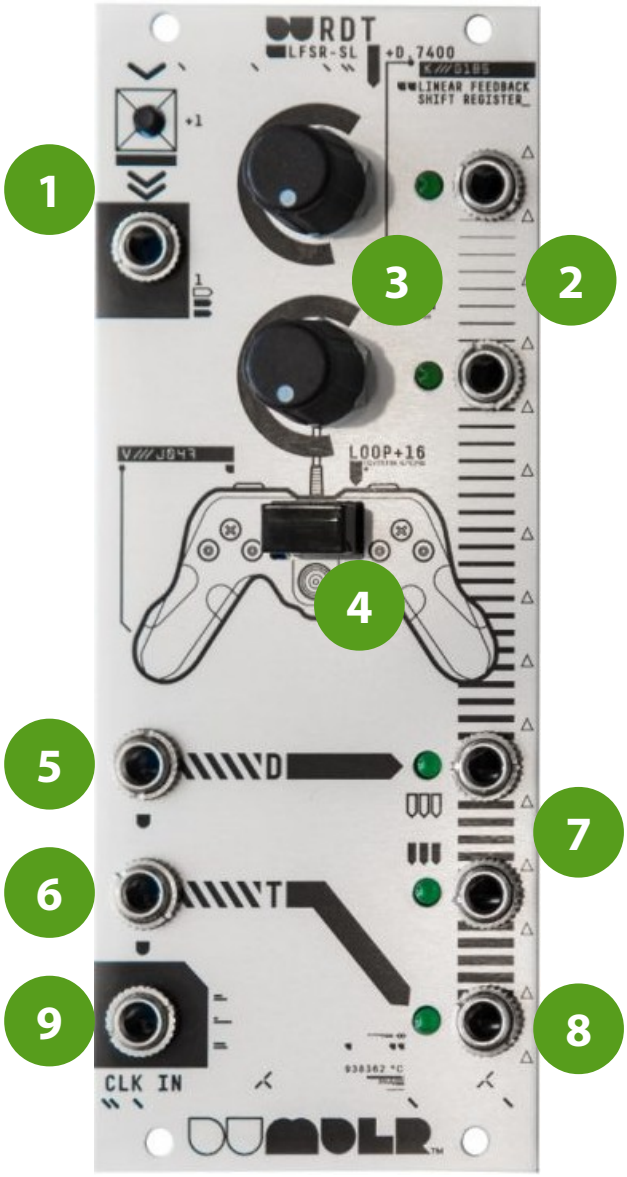
RDT LFSR-SL +D.7400 K///G185

LINEAR FEEDBACK SHIFT REGISTER_



1 16-bit register input
Connect a gate, LFO, etc. or press the button to push pulses into the 16-bit register. When disconnected, a linear feedback shift register (random pattern generator) is used.

2 16-bit register outputs
Output pulse time is tied to the clock signal.

3 Register delay selectors
Select the bit position (delay) of each register output – bits 1 to 8 above, bits 9 to 16 below.

4 Pattern lock switch
Locks and repeats the current register pattern.

5 D flip flop input
Gating will set the output on a clock pulse.

6 T flip flop input
Gating will toggle the output on a clock pulse.

7 D flip flop outputs
Delay input by one clock pulse. Clock divider (÷2) with input disconnected. Lower output is inverted.

8 T flip flop output
Toggled each clock by input. Toggled by inverted D flip flop output with input disconnected; clock divider (÷4) if neither flip flop input is connected.

9 Clock input
Must be connected; outputs only change with clock pulses. Use any clock, gate, trigger, or even audio-rate signal.

Width: 10HP · Depth: 20.5mm · Current draw: 60mA 5V

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