# **NOISE BOM V8, 2020**

## Noise PCB & Panel

Noiso i ob a i unoi			
Resistors R1, R10, R11, R12 R2, R3, R4, R9, R13, R16, R17,	4	100R	
R18, R20, R21, R22, R24	12	1K	
R5, R6, R7, R8	4	1M	Alt: 1.2M
F1	1	10R	Alt: ferrite bead
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Diodes			
		7	ALT DND
D1, D2, D3, D4	4	Zener 9.1v	ALT: DNP
D6	1	1N4001	
IC's			
IC1	1	16 PIN	
IC2	1	14 PIN	
Capacitors			
C1, C2	2	100NF	2.5mm film
C3	1	470PF	2.5mm ceramic
C4	1	330NF	2.5mm
C5	-		5mm film
	1	470NF	
C6, C7, C12	3	10UF	Electrolythic
C8, C9, C10, C13	4	4.7UF	Electrolythic
C11	1	220UF	Electrolythic
Transistors			
T1, T2, T3, T4	4	2N3904	
Power header			
SV1	1	10 pin shrouded	
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Headers			
Z1, Z2	2	1X2 pin	Dunont male
			Dupont male
PATCH	1	2X6 pin	Dupont male
FEMALE DUPONT CABLE	4		
JUMPERS	2		
Jacks / Pots			
CV1,CV2, CV3, CV4	4	Thonkiconn	
OUT1, OUT2, OUT3, OUT4	4	Thonkiconn	
FREQ1, FREQ2, FREQ3, FREQ4	4	B10K vertical 9mm	Alpha
Knobs	4		•
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## Patchbay

**Screws** 

There are 8 outputs and 4 inputs on the patchbay.

4 6mm M3

#### Blad1

The 8 outputs are connected to the CD4040 Divider, each is a lower division of the main output frequency.

Patch 1 or 2 female Dupont cables from the 8 outputs to input "3" or (inversed) "4" These signals are mixed together to get new combinations of noise!

Position 3 = output 3

Position 4 (inverted symbol) = output 4

#### **MODS**

Z1 and Z2 determine the range of OSC1 and OSC2. Place a jumper there to lower the frequencies. Capacitor C11 determines the alternative frequency for OSC1, C12 determines the alternative frequency for OSC2.

The patchheader sets the output patch of OUT1 and OUT2. There are 8 possible frequency divisions. Instead of patching these with female Dupont cables, you could make a expander panel. Idea's: touchplates, banana patchbay, automatic switching with a CD4066 or CD4053